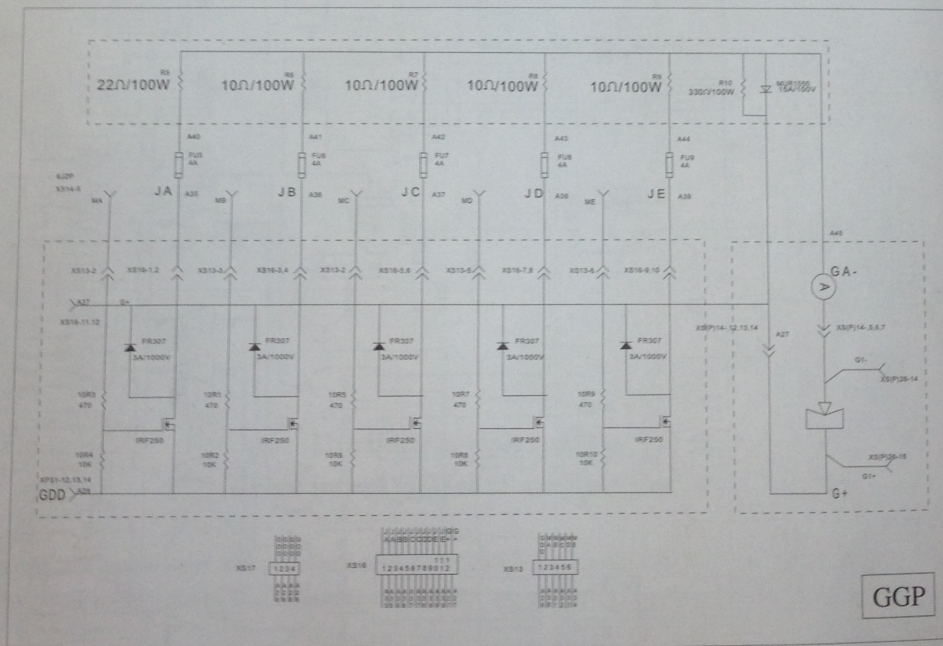


The instruction for the GZP5 panel signal

- There are 4 adapter sockets out-connect on the panel.
- N1, N2, N3, N4 are pulse width controlling signals, they are connected to the interface board on the computer by D3, D4, D5, D6.
- K1, K2, K3, K4, are pulse distance controlling signals, they are connected to the interface board on the computer by D7, D8, D9, D10.
- I1, I2, I3, I4, I5, I6, I7, I8, are currency controlling signals connected to the interface board on the computer by D11, D12, D13, D14, D15, D16, D17, D18.
- MA, MB, MC, ME are out-put signals of principal oscillating board connected to the HF power board by A30, A31, A32, A33, A34.
- YD is out-put interface board. When YD is validity, the signal is 12v, inherent resistance is about 500 when it is invalidity, the voltage is 0.
- JD, 12DD are the HF controlling signal and the earth of the 12v signal output by the frequency conversion board.
- 12V is the 12v out-put signal on the frequency conversion board connected to the principal oscillating board by A59.
- VGA, VGB, VGC. 14V are 3phase A.C. signals connected to the GZP5 panel by A3, A4, A5.



The instruction for the GGP panel signal

Ma, Mb, Mc, Md, Me are the out-put signals connected from the HF principal oscillating board JZP to gate of HF power MOS pipe by the flow-line A30-A34. The out-put signal JA-JE of the gate of MOS pipe is connected to the safety tube FU5-FU9 by A35-A39, and to HF resistance R5-R9 by A40-A44.