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john@john-OptiPlex-380:~$ halrun
halcmd: loadrt hostmot2
Note: Using POSIX realtime
hm2: loading Mesa HostMot2 driver version 0.15
halcmd: loadrt hm2_pci
hm2_pci: loading Mesa AnyIO HostMot2 driver version 0.7
hm2_pci: discovered 5i24 at 0000:03:02.0
hm2/hm2_5i24.0: Smart Serial Firmware Version 43
Board hm2_5i24.0.7i70.0.0 Hardware Mode 0 = standard
Board hm2_5i24.0.7i70.0.0 Software Mode 0 = input_only
Board hm2_5i24.0.7i70.0.0 Software Mode 1 = input_analog
Board hm2_5i24.0.7i70.0.0 Software Mode 2 = input_fieldvoltage
Board hm2_5i24.0.7i70.0.1 Hardware Mode 0 = standard
Board hm2_5i24.0.7i70.0.1 Software Mode 0 = input_only
Board hm2_5i24.0.7i70.0.1 Software Mode 1 = input_analog
Board hm2_5i24.0.7i70.0.1 Software Mode 2 = input_fieldvoltage
Board hm2_5i24.0.7i72.0.2 Hardware Mode 0 = standard
Board hm2_5i24.0.7i72.0.2 Software Mode 0 = output_only
Board hm2_5i24.0.7i72.0.2 Software Mode 1 = output_fieldvoltage
Board hm2_5i24.0.7i83.0.4 Hardware Mode 0 = standard
Board hm2_5i24.0.7i83.0.4 Software Mode 0 = analogout_enables
hm2/hm2_5i24.0: 72 I/O Pins used:
hm2/hm2_5i24.0: IO Pin 000 (P4-01): PWMGen #0, pin Not-Enable (Output)
hm2/hm2_5i24.0: IO Pin 001 (P4-03): Muxed Encoder #0, pin Muxed A (Input)
hm2/hm2_5i24.0: IO Pin 002 (P4-05): Muxed Encoder #0, pin Muxed B (Input)
hm2/hm2_5i24.0: IO Pin 003 (P4-07): Muxed Encoder #0, pin Muxed Index (Input)
hm2/hm2_5i24.0: IO Pin 004 (P4-09): Muxed Encoder #1, pin Muxed A (Input)
hm2/hm2_5i24.0: IO Pin 005 (P4-11): Muxed Encoder #1, pin Muxed B (Input)
hm2/hm2_5i24.0: IO Pin 006 (P4-13): Muxed Encoder #1, pin Muxed Index (Input)
hm2/hm2_5i24.0: IO Pin 007 (P4-15): Muxed Encoder #2, pin Muxed A (Input)
hm2/hm2_5i24.0: IO Pin 008 (P4-17): Muxed Encoder #2, pin Muxed B (Input)
hm2/hm2_5i24.0: IO Pin 009 (P4-19): Muxed Encoder #2, pin Muxed Index (Input)
hm2/hm2_5i24.0: IO Pin 010 (P4-21): Muxed Encoder Select #0, pin Mux Select 0 (Output)
hm2/hm2_5i24.0: IO Pin 011 (P4-23): PWMGen #0, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 012 (P4-25): PWMGen #0, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 013 (P4-27): PWMGen #1, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 014 (P4-29): PWMGen #1, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 015 (P4-31): PWMGen #2, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 016 (P4-33): PWMGen #2, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 017 (P4-35): PWMGen #3, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 018 (P4-37): PWMGen #3, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 019 (P4-39): PWMGen #4, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 020 (P4-41): PWMGen #4, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 021 (P4-43): PWMGen #5, pin Out0 (PWM or Up) (Output)
hm2/hm2_5i24.0: IO Pin 022 (P4-45): PWMGen #5, pin Out1 (Dir or Down) (Output)
hm2/hm2_5i24.0: IO Pin 023 (P4-47): PWMGen #0, pin Not-Enable (Output)
hm2/hm2_5i24.0: IO Pin 024 (P3-01): IOPort
hm2/hm2_5i24.0: IO Pin 025 (P3-03): IOPort
hm2/hm2_5i24.0: IO Pin 026 (P3-05): IOPort
hm2/hm2_5i24.0: IO Pin 027 (P3-07): IOPort
hm2/hm2_5i24.0: IO Pin 028 (P3-09): IOPort
hm2/hm2_5i24.0: IO Pin 029 (P3-11): IOPort
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hm2/hm2_5i24.0: IO Pin 030 (P3-13): IOPort
hm2/hm2_5i24.0: IO Pin 031 (P3-15): IOPort
hm2/hm2_5i24.0: IO Pin 032 (P3-17): IOPort
hm2/hm2_5i24.0: IO Pin 033 (P3-19): IOPort
hm2/hm2_5i24.0: IO Pin 034 (P3-21): IOPort
hm2/hm2_5i24.0: IO Pin 035 (P3-23): IOPort
hm2/hm2_5i24.0: IO Pin 036 (P3-25): IOPort
hm2/hm2_5i24.0: IO Pin 037 (P3-27): IOPort
hm2/hm2_5i24.0: IO Pin 038 (P3-29): IOPort
hm2/hm2_5i24.0: IO Pin 039 (P3-31): IOPort
hm2/hm2_5i24.0: IO Pin 040 (P3-33): IOPort
hm2/hm2_5i24.0: IO Pin 041 (P3-35): IOPort
hm2/hm2_5i24.0: IO Pin 042 (P3-37): IOPort
hm2/hm2_5i24.0: IO Pin 043 (P3-39): IOPort
hm2/hm2_5i24.0: IO Pin 044 (P3-41): IOPort
hm2/hm2_5i24.0: IO Pin 045 (P3-43): IOPort
hm2/hm2_5i24.0: IO Pin 046 (P3-45): IOPort
hm2/hm2_5i24.0: IO Pin 047 (P3-47): IOPort
hm2/hm2_5i24.0: IO Pin 048 (P2-01): Smart Serial Interface #0, pin RxData0 (Input)
hm2/hm2_5i24.0: IO Pin 049 (P2-03): Smart Serial Interface #0, pin RxData1 (Input)
hm2/hm2_5i24.0: IO Pin 050 (P2-05): Smart Serial Interface #0, pin RxData2 (Input)
hm2/hm2_5i24.0: IO Pin 051 (P2-07): IOPort
hm2/hm2_5i24.0: IO Pin 052 (P2-09): Smart Serial Interface #0, pin TxData0 (Output)
hm2/hm2_5i24.0: IO Pin 053 (P2-11): Smart Serial Interface #0, pin TxEn0 (Output)
hm2/hm2_5i24.0: IO Pin 054 (P2-13): Smart Serial Interface #0, pin TxData1 (Output)
hm2/hm2_5i24.0: IO Pin 055 (P2-15): Smart Serial Interface #0, pin TxEn1 (Output)
hm2/hm2_5i24.0: IO Pin 056 (P2-17): Smart Serial Interface #0, pin TxData2 (Output)
hm2/hm2_5i24.0: IO Pin 057 (P2-19): Smart Serial Interface #0, pin TxEn2 (Output)
hm2/hm2_5i24.0: IO Pin 058 (P2-21): IOPort
hm2/hm2_5i24.0: IO Pin 059 (P2-23): IOPort
hm2/hm2_5i24.0: IO Pin 060 (P2-25): Smart Serial Interface #0, pin RxData4 (Input)
hm2/hm2_5i24.0: IO Pin 061 (P2-27): IOPort
hm2/hm2_5i24.0: IO Pin 062 (P2-29): IOPort
hm2/hm2_5i24.0: IO Pin 063 (P2-31): IOPort
hm2/hm2_5i24.0: IO Pin 064 (P2-33): Smart Serial Interface #0, pin TxData4 (Output)
hm2/hm2_5i24.0: IO Pin 065 (P2-35): Smart Serial Interface #0, pin TxEn4 (Output)
hm2/hm2_5i24.0: IO Pin 066 (P2-37): IOPort
hm2/hm2_5i24.0: IO Pin 067 (P2-39): IOPort
hm2/hm2_5i24.0: IO Pin 068 (P2-41): IOPort
hm2/hm2_5i24.0: IO Pin 069 (P2-43): IOPort
hm2/hm2_5i24.0: IO Pin 070 (P2-45): IOPort
hm2/hm2_5i24.0: IO Pin 071 (P2-47): IOPort
hm2/hm2_5i24.0: registered
hm2_5i24.0: initialized AnyIO board at 0000:03:02.0
halcmd: addf hm2_5i24.0.read thread1
HAL: ERROR: thread 'thread1' not found
<stdin>:3: addf failed
halcmd: loadrt threads
halcmd: addf hm2_5i24.read thread1
HAL: ERROR: function 'hm2_5i24.read' not found
<stdin>:5: addf failed
halcmd: show pin
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Component Pins:

Owner	Type	Dir	Value	Name
7	s32	OUT	0	hm2_5i24.0.0.debug
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-00
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-00-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-01
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-01-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-02
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-02-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-03
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-03-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-04
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-04-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-05
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-05-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-06
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-06-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-07
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-07-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-08
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-08-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-09
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-09-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-10
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-10-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-11
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-11-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-12
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-12-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-13
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-13-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-14
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-14-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-15
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-15-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-16
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-16-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-17
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-17-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-18
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-18-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-19
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-19-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-20
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-20-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-21
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-21-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-22
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-22-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-23
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-23-not
7	bit	OUT	FALSE	hm2_5i24.0.7i70.0.0.input-24







7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-13
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-14
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-15
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-16
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-17
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-18
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-19
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-20
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-21
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-22
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-23
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-24
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-25
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-26
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-27
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-28
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-29
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-30
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-31
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-32
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-33
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-34
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-35
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-36
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-37
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-38
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-39
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-40
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-41
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-42
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-43
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-44
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-45
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-46
7 bit IN	FALSE	hm2_5i24.0.7i72.0.2.output-47
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena0
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena1
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena2
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena3
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena4
7 bit IN	FALSE	hm2_5i24.0.7i83.0.4.analogena5
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout0
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout1
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout2
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout3
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout4
7 float IN	0	hm2_5i24.0.7i83.0.4.analogout5
7 float OUT	0	hm2_5i24.0.7i83.0.4.fieldvoltage
7 s32 OUT	0	hm2_5i24.0.encoder.00.count
7 s32 OUT	0	hm2_5i24.0.encoder.00.count-latched
7 bit I/O	FALSE	hm2_5i24.0.encoder.00.index-enable
7 bit OUT	TRUE	hm2_5i24.0.encoder.00.input-a

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7 bit OUT      TRUE hm2_5i24.0.encoder.00.input-b
7 bit OUT      FALSE hm2_5i24.0.encoder.00.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.00.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.00.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.00.position
7 float OUT    0 hm2_5i24.0.encoder.00.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.00.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.00.quad-error-enable
7 s32 OUT      2 hm2_5i24.0.encoder.00.rawcounts
7 s32 OUT      2 hm2_5i24.0.encoder.00.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.00.reset
7 float OUT    0 hm2_5i24.0.encoder.00.velocity
7 float OUT    0 hm2_5i24.0.encoder.00.velocity-rpm
7 s32 OUT      0 hm2_5i24.0.encoder.01.count
7 s32 OUT      0 hm2_5i24.0.encoder.01.count-latched
7 bit I/O      FALSE hm2_5i24.0.encoder.01.index-enable
7 bit OUT      TRUE hm2_5i24.0.encoder.01.input-a
7 bit OUT      TRUE hm2_5i24.0.encoder.01.input-b
7 bit OUT      FALSE hm2_5i24.0.encoder.01.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.01.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.01.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.01.position
7 float OUT    0 hm2_5i24.0.encoder.01.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.01.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.01.quad-error-enable
7 s32 OUT      65534 hm2_5i24.0.encoder.01.rawcounts
7 s32 OUT      65534 hm2_5i24.0.encoder.01.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.01.reset
7 float OUT    0 hm2_5i24.0.encoder.01.velocity
7 float OUT    0 hm2_5i24.0.encoder.01.velocity-rpm
7 s32 OUT      0 hm2_5i24.0.encoder.02.count
7 s32 OUT      0 hm2_5i24.0.encoder.02.count-latched
7 bit I/O      FALSE hm2_5i24.0.encoder.02.index-enable
7 bit OUT      FALSE hm2_5i24.0.encoder.02.input-a
7 bit OUT      TRUE hm2_5i24.0.encoder.02.input-b
7 bit OUT      FALSE hm2_5i24.0.encoder.02.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.02.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.02.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.02.position
7 float OUT    0 hm2_5i24.0.encoder.02.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.02.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.02.quad-error-enable
7 s32 OUT      1 hm2_5i24.0.encoder.02.rawcounts
7 s32 OUT      1 hm2_5i24.0.encoder.02.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.02.reset
7 float OUT    0 hm2_5i24.0.encoder.02.velocity
7 float OUT    0 hm2_5i24.0.encoder.02.velocity-rpm
7 s32 OUT      0 hm2_5i24.0.encoder.03.count
7 s32 OUT      0 hm2_5i24.0.encoder.03.count-latched
7 bit I/O      FALSE hm2_5i24.0.encoder.03.index-enable
7 bit OUT      TRUE hm2_5i24.0.encoder.03.input-a
7 bit OUT      TRUE hm2_5i24.0.encoder.03.input-b

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7 bit OUT      TRUE hm2_5i24.0.encoder.03.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.03.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.03.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.03.position
7 float OUT    0 hm2_5i24.0.encoder.03.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.03.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.03.quad-error-enable
7 s32 OUT      0 hm2_5i24.0.encoder.03.rawcounts
7 s32 OUT      0 hm2_5i24.0.encoder.03.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.03.reset
7 float OUT    0 hm2_5i24.0.encoder.03.velocity
7 float OUT    0 hm2_5i24.0.encoder.03.velocity-rpm
7 s32 OUT      0 hm2_5i24.0.encoder.04.count
7 s32 OUT      0 hm2_5i24.0.encoder.04.count-latched
7 bit I/O      FALSE hm2_5i24.0.encoder.04.index-enable
7 bit OUT      TRUE hm2_5i24.0.encoder.04.input-a
7 bit OUT      TRUE hm2_5i24.0.encoder.04.input-b
7 bit OUT      TRUE hm2_5i24.0.encoder.04.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.04.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.04.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.04.position
7 float OUT    0 hm2_5i24.0.encoder.04.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.04.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.04.quad-error-enable
7 s32 OUT      4 hm2_5i24.0.encoder.04.rawcounts
7 s32 OUT      4 hm2_5i24.0.encoder.04.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.04.reset
7 float OUT    0 hm2_5i24.0.encoder.04.velocity
7 float OUT    0 hm2_5i24.0.encoder.04.velocity-rpm
7 s32 OUT      0 hm2_5i24.0.encoder.05.count
7 s32 OUT      0 hm2_5i24.0.encoder.05.count-latched
7 bit I/O      FALSE hm2_5i24.0.encoder.05.index-enable
7 bit OUT      FALSE hm2_5i24.0.encoder.05.input-a
7 bit OUT      FALSE hm2_5i24.0.encoder.05.input-b
7 bit OUT      TRUE hm2_5i24.0.encoder.05.input-index
7 bit IN       FALSE hm2_5i24.0.encoder.05.latch-enable
7 bit IN       FALSE hm2_5i24.0.encoder.05.latch-polarity
7 float OUT    0 hm2_5i24.0.encoder.05.position
7 float OUT    0 hm2_5i24.0.encoder.05.position-latched
7 bit OUT      FALSE hm2_5i24.0.encoder.05.quad-error
7 bit IN       FALSE hm2_5i24.0.encoder.05.quad-error-enable
7 s32 OUT      2 hm2_5i24.0.encoder.05.rawcounts
7 s32 OUT      2 hm2_5i24.0.encoder.05.rawlatch
7 bit IN       FALSE hm2_5i24.0.encoder.05.reset
7 float OUT    0 hm2_5i24.0.encoder.05.velocity
7 float OUT    0 hm2_5i24.0.encoder.05.velocity-rpm
7 u32 IN       0x007F2815 hm2_5i24.0.encoder.muxed-sample-frequency
7 bit OUT      TRUE hm2_5i24.0.gpio.000.in
7 bit OUT      FALSE hm2_5i24.0.gpio.000.in_not
7 bit OUT      TRUE hm2_5i24.0.gpio.001.in
7 bit OUT      FALSE hm2_5i24.0.gpio.001.in_not
7 bit OUT      TRUE hm2_5i24.0.gpio.002.in

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7 bit	OUT	FALSE	hm2_5i24.0.gpio.002.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.003.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.003.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.004.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.004.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.005.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.005.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.006.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.006.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.007.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.007.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.008.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.008.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.009.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.009.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.010.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.010.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.011.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.011.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.012.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.012.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.013.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.013.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.014.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.014.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.015.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.015.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.016.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.016.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.017.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.017.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.018.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.018.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.019.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.019.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.020.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.020.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.021.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.021.in_not
7 bit	OUT	FALSE	hm2_5i24.0.gpio.022.in
7 bit	OUT	TRUE	hm2_5i24.0.gpio.022.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.023.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.023.in_not
7 bit	OUT	TRUE	hm2_5i24.0.gpio.024.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.024.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.024.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.025.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.025.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.025.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.026.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.026.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.026.out

7 bit	OUT	TRUE	hm2_5i24.0.gpio.027.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.027.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.027.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.028.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.028.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.028.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.029.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.029.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.029.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.030.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.030.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.030.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.031.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.031.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.031.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.032.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.032.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.032.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.033.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.033.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.033.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.034.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.034.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.034.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.035.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.035.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.035.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.036.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.036.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.036.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.037.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.037.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.037.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.038.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.038.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.038.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.039.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.039.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.039.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.040.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.040.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.040.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.041.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.041.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.041.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.042.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.042.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.042.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.043.in
7 bit	OUT	FALSE	hm2_5i24.0.gpio.043.in_not
7 bit	IN	FALSE	hm2_5i24.0.gpio.043.out
7 bit	OUT	TRUE	hm2_5i24.0.gpio.044.in

7 bit OUT FALSE hm2\_5i24.0.gpio.044.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.044.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.045.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.045.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.045.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.046.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.046.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.046.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.047.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.047.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.047.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.048.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.048.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.049.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.049.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.050.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.050.in\_not  
7 bit OUT FALSE hm2\_5i24.0.gpio.051.in  
7 bit OUT TRUE hm2\_5i24.0.gpio.051.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.051.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.052.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.052.in\_not  
7 bit OUT FALSE hm2\_5i24.0.gpio.053.in  
7 bit OUT TRUE hm2\_5i24.0.gpio.053.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.054.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.054.in\_not  
7 bit OUT FALSE hm2\_5i24.0.gpio.055.in  
7 bit OUT TRUE hm2\_5i24.0.gpio.055.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.056.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.056.in\_not  
7 bit OUT FALSE hm2\_5i24.0.gpio.057.in  
7 bit OUT TRUE hm2\_5i24.0.gpio.057.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.058.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.058.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.058.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.059.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.059.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.059.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.060.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.060.in\_not  
7 bit OUT TRUE hm2\_5i24.0.gpio.061.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.061.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.061.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.062.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.062.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.062.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.063.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.063.in\_not  
7 bit IN FALSE hm2\_5i24.0.gpio.063.out  
7 bit OUT TRUE hm2\_5i24.0.gpio.064.in  
7 bit OUT FALSE hm2\_5i24.0.gpio.064.in\_not  
7 bit OUT FALSE hm2\_5i24.0.gpio.065.in

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7 bit OUT      TRUE hm2_5i24.0.gpio.065.in_not
7 bit OUT      TRUE hm2_5i24.0.gpio.066.in
7 bit OUT      FALSE hm2_5i24.0.gpio.066.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.066.out
7 bit OUT      TRUE hm2_5i24.0.gpio.067.in
7 bit OUT      FALSE hm2_5i24.0.gpio.067.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.067.out
7 bit OUT      TRUE hm2_5i24.0.gpio.068.in
7 bit OUT      FALSE hm2_5i24.0.gpio.068.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.068.out
7 bit OUT      TRUE hm2_5i24.0.gpio.069.in
7 bit OUT      FALSE hm2_5i24.0.gpio.069.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.069.out
7 bit OUT      TRUE hm2_5i24.0.gpio.070.in
7 bit OUT      FALSE hm2_5i24.0.gpio.070.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.070.out
7 bit OUT      TRUE hm2_5i24.0.gpio.071.in
7 bit OUT      FALSE hm2_5i24.0.gpio.071.in_not
7 bit IN       FALSE hm2_5i24.0.gpio.071.out
7 bit IN       FALSE hm2_5i24.0.led.CR01
7 bit IN       FALSE hm2_5i24.0.led.CR02
7 bit IN       FALSE hm2_5i24.0.pwmgen.00.enable
7 float IN     0 hm2_5i24.0.pwmgen.00.value
7 bit IN       FALSE hm2_5i24.0.pwmgen.01.enable
7 float IN     0 hm2_5i24.0.pwmgen.01.value
7 bit IN       FALSE hm2_5i24.0.pwmgen.02.enable
7 float IN     0 hm2_5i24.0.pwmgen.02.value
7 bit IN       FALSE hm2_5i24.0.pwmgen.03.enable
7 float IN     0 hm2_5i24.0.pwmgen.03.value
7 bit IN       FALSE hm2_5i24.0.pwmgen.04.enable
7 float IN     0 hm2_5i24.0.pwmgen.04.value
7 bit IN       FALSE hm2_5i24.0.pwmgen.05.enable
7 float IN     0 hm2_5i24.0.pwmgen.05.value
7 s32 OUT     0 hm2_5i24.0.read.time
7 s32 OUT     0 hm2_5i24.0.read_gpio.time
7 u32 OUT    0x00000000 hm2_5i24.0.sserial.port-0.fault-count
7 u32 OUT    0x00000000 hm2_5i24.0.sserial.port-0.port_state
7 u32 OUT    0x00000000 hm2_5i24.0.sserial.port-0.port_state2
7 u32 OUT    0x00000000 hm2_5i24.0.sserial.port-0.port_state3
7 bit IN      TRUE hm2_5i24.0.sserial.port-0.run
7 bit I/O    FALSE hm2_5i24.0.watchdog.has_bit
7 s32 OUT     0 hm2_5i24.0.write.time
7 s32 OUT     0 hm2_5i24.0.write_gpio.time
11 s32 OUT    0 thread1.time

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halcmd: