

Advanced

USB Configuration

USB Module Version 8.11.03

USB Devices:

1 Keyboard, 1 Mouse, 2 Hubs

Legacy USB Support [Enabled]

XHCI Hand-off [Enabled]

EHCI Hand-off [Disabled]

USB Mass Storage Driver Support [Enabled]

Port 60/64 Emulation [Enabled]

USB hardware delays and time-outs:

USB transfer time-out [20 sec]

Device reset time-out [20 sec]

Device power-up delay [Auto]

Enables Legacy USB support. AUTO option disables legacy support if no USB devices are connected. DISABLE option will keep USB devices available only for EFI applications.

←→: Select Screen  
 F4: Select Item  
 Enter: Select  
 +/=: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
 F10: Save & Exit  
 ESC: Exit

Advanced

PORT CONFIGURATION MENU

► NIC Configuration

Blink LEDs 0

PORT CONFIGURATION INFORMATION

UEFI Driver: Intel(R) PRO/1000  
6.1.16 PCI-E  
Adapter PBA: FFFFFFFF-OFF  
Chip Type: Intel 82574  
PCI Device ID: 1003  
PCI Address: 02:00:00  
Link Status: [Disconnected]  
MAC Address: 00:E8:4C:68:FC:5B

Click to configure the network device port.

→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Advanced

PORT CONFIGURATION MENU

► NIC Configuration

Blink LEDs: 0

PORT CONFIGURATION INFORMATION

UEFI Driver: Intel(R) PRO/1000  
6.1.16 PCI-E  
Adapter PBA: FFFFFFFF-OFF  
Chip Type: Intel 82574  
PCI Device ID: 1003  
PCI Address: 03:00:00  
Link Status: [Disconnected]  
MAC Address: 00:EB:4C:68:FC:5C

Click to configure the network device port.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Advanced

▶ Intel(R) PRO/1000 6.1.16 PCI-E Healthy

Provides Health Status for the Drivers/Controllers

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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Main Advanced **Chipset** Security Boot Save & Exit

- ▶ System Agent (SA) Configuration
- ▶ PCH-I/O Configuration

System Agent (SA) Parameters

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

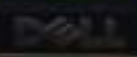
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Chipset

System Agent Bridge Name	Broadwell	Check to enable VT-d function on MCH.
System Agent RC Version	2.5.0.0	
VT-d	[Disabled]	
CHAP Device (B0:D7:F0)	[Disabled]	
Thermal Device (B0:D4:F0)	[Enabled]	
CPU SA Audio Device (B0:D3:F0)	[Disabled]	
Enable NB CRID	[Disabled]	
Above 4GB MMIO BIOS assignent	[Disabled]	
BOAT ACPI Table Support	[Disabled]	

- ▶ Graphics Configuration
- ▶ DMI/OPI Configuration
- ▶ Memory Configuration
- ▶ Memory Thermal Configuration
- ▶ GT - Power Management Control
- ▶ NB Porting Configuration

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit



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Chipset

Graphics Configuration

IGFX VBIOS Version 1036  
IGfx Frequency 600 MHz  
Graphics Turbo IMON Current 31

Graphics turbo IMON current values supported (14-31)

Primary Display [Auto]  
  Primary REG [Auto]  
  Primary PCIE [Auto]  
Internal Graphics [Auto]  
GTT Size [8MB]  
Aperture Size [256MB]  
CD CLK Frequency [Auto]  
DVMT Pre-Allocated [256M]  
DVMT Total Gfx Mem [256M]  
Gfx Low Power Mode [Enabled]  
ALS Support [Disabled]  
▶ LCD Control

++: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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DELL

DLINK Configuration

DLINK	192.168.1.1
DLINK VLS Control	Disabled
DLINK VLS Control	Disabled
DLINK VLS Control	Disabled
DLINK Link Aggr Control	Disabled
DLINK Enhanced Turbo Control	Disabled
DLINK WOL	Disabled
DLINK Wake-on-LAN Control	Disabled
DLINK WOL	Disabled

Factory Default (DLINK)

- 00. Setup Screen
- 01. Setup Page
- 02. Setup
- 03. Change IP
- 04. General Setup
- 05. Advanced Setup
- 06. Advanced Defaults
- 07. Save & Exit
- 08. Exit



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Chipset

Memory Information

Memory RC Version	2.5.0.3
Memory Frequency	1600 MHz
Total Memory	8192 MB (DDR3)
Memory Voltage	1.35v
DIMM0	Not Present
DIMM2	8192 MB (DDR3)
CAS Latency (tCL)	11
Minimum delay time	
CAS to RAS (tRCdmin)	11
Row Precharge (tRPmin)	11
Active to Precharge (tRASmin)	28
XMP Profile 1	Not Supported
XMP Profile 2	Not Supported

DIMM profile

Memory Frequency Limiter	[Default DIMM profile]
ECC Support	[Auto]
Hax TOLUD	[Enabled]
Enh Interleave Support	[Dynamic]
R1 Support	[Enabled]
DLL Weak Lock Support	[Enabled]
Enable RN Prevention	[Enabled]
Row Hammer Solution	[Hardware-RHP]

▲ Select DIMM timing profile that should be used.

↔: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
 F10: Save & Exit  
 ESC: Exit

Chipset

XMP Profile 1	Not Supported	▲ Enable or disable GDXC
XMP Profile 2	Not Supported	
DIMM profile	[Default DIMM profile]	
Memory Frequency Limiter	[Auto]	
ECC Support	[Enabled]	
Max TOLUD	[Dynamic]	
Enh Interleave Support	[Enabled]	
RI Support	[Enabled]	
DLL Weak Lock Support	[Enabled]	
Enable RH Prevention	[Enabled]	
Row Hammer Solution	[Hardware RHP]	
RH Activation Probability	[1/2 <sup>14</sup> ]	
Enable RH Keep Seeds	[Disabled]	
Hc Lock	[Enabled]	
Ch Hash Support	[Auto]	
NMode Support	[Auto]	
Memory Scrambler	[Disabled]	
RMT Crosser Support	[Disabled]	
HRC Fast Boot	[Enabled]	
DIMM Exit Mode	[Auto]	
Memory Remap	[Enabled]	
Channel A DIMM Control	[Enable Both DIMMS]	
Channel B DIMM Control	[Enable Both DIMMS]	
GDXC Support	[Disabled]	

- ←→: Select Screen
- ↑↓: Select Item
- Enter: Select
- +/-: Change Opt.
- F1: General Help
- F2: Previous Values
- F9: Optimized Defaults
- F10: Save & Exit
- ESC: Exit

Chipset

Memory Thermal Configuration

▶ Memory Power and Thermal Throttling  
Memory Thermal Management [Disabled]

Memory Power and Thermal Throttling Options

⇐: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Chipset

Memory Power and Thermal Throttling

DDR PowerDown and idle counter	{BIOS}
Refresh 2x Support	[Disabled]
LPDDR Thermal Sensor	[Enabled]
SelfRefresh Enable	[Enabled]
SelfRefresh IdleTimer	512
Throttler CKEMin Defeature	[Disabled]
Throttler CKEMin Timer	48

- ▶ Dram Power Meter
- ▶ Memory Thermal Reporting
- ▶ Memory RAFL

BIOS: BIOS is in control of DDR CKE mode and idle timer value. FCODE: fcode will manage the modes.

⇐: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Chipset

Dram Power Meter

Use user provided power weights,  
scale factor, and channel power floor values.

User Power Weights Enable [Disabled]

Energy Scale Fact 4

Idle Energy Ch0Dimm0 10

PowerDown Energy Ch0Dimm0 6

Activate Energy Ch0Dimm0 172

Read Energy Ch0Dimm0 212

Write Energy Ch0Dimm0 221

Idle Energy Ch0Dimm1 10

PowerDown Energy Ch0Dimm1 6

Activate Energy Ch0Dimm1 172

Read Energy Ch0Dimm1 212

Write Energy Ch0Dimm1 221

Idle Energy Ch1Dimm0 10

PowerDown Energy Ch1Dimm0 6

Activate Energy Ch1Dimm0 172

Read Energy Ch1Dimm0 212

Write Energy Ch1Dimm0 221

▲ Enable: User provided power weights, scale factor, and channel power floor values are used. Disable: BIOS sets power weights, scale factor, and channel power floor values based on DIMMs present in system.

←→: Select Screen

F4: Select Item

Enter: Select

+/-: Change Opt.

F1: General Help

F2: Previous Values

F9: Optimized Defaults

F10: Save & Exit

ESC: Exit

Chipset

Energy Scale Fact	4
Idle Energy Ch0Dimm0	10
PowerDown Energy Ch0Dimm0	6
Activate Energy Ch0Dimm0	172
Read Energy Ch0Dimm0	212
Write Energy Ch0Dimm0	221
Idle Energy Ch0Dimm1	10
PowerDown Energy Ch0Dimm1	6
Activate Energy Ch0Dimm1	172
Read Energy Ch0Dimm1	212
Write Energy Ch0Dimm1	221
Idle Energy Ch1Dimm0	10
PowerDown Energy Ch1Dimm0	6
Activate Energy Ch1Dimm0	172
Read Energy Ch1Dimm0	212
Write Energy Ch1Dimm0	221
Idle Energy Ch1Dimm1	10
PowerDown Energy Ch1Dimm1	6
Activate Energy Ch1Dimm1	172
Read Energy Ch1Dimm1	212

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Chipset

Lock Thermal Management Registers [Disabled]

Memory Thermal Reporting

Extern Thermal Status [Disabled]

Closed Loop Therm Manage [Disabled]

Open Loop Therm Manage [Disabled]

Thermal Threshold Settings

Warm Threshold Ch0 Dimm0 255

Warm Threshold Ch0 Dimm1 255

Hot Threshold Ch0 Dimm0 255

Hot Threshold Ch0 Dimm1 255

Warm Threshold Ch1 Dimm0 255

Warm Threshold Ch1 Dimm1 255

Hot Threshold Ch1 Dimm0 255

Hot Threshold Ch1 Dimm1 255

Thermal Throttle Budget Settings:

Warm Budget Ch0 Dimm0 255

Warm Budget Ch0 Dimm1 255

Hot Budget Ch0 Dimm0 255

Hot Budget Ch0 Dimm1 255

▲ Enable or disable Lock several PDU registers related to DDR power Thermal Management.

- ←→: Select Screen
- ↑↓: Select Item
- Enter: Select
- +/-: Change Opt.
- F1: General Help
- F2: Previous Values
- F9: Optimized Defaults
- F10: Save & Exit
- ESC: Exit

Chipset

Extern Therm Status [Disabled]  
Closed Loop Therm Manage [Disabled]  
Open Loop Therm Manage [Disabled]

Thermal Threshold Settings

Warm Threshold Ch0 Dimm0	255
Warm Threshold Ch0 Dimm1	255
Hot Threshold Ch0 Dimm0	255
Hot Threshold Ch0 Dimm1	255
Warm Threshold Ch1 Dimm0	255
Warm Threshold Ch1 Dimm1	255
Hot Threshold Ch1 Dimm0	255
Hot Threshold Ch1 Dimm1	255

Thermal Throttle Budget Settings

Warm Budget Ch0 Dimm0	255
Warm Budget Ch0 Dimm1	255
Hot Budget Ch0 Dimm0	255
Hot Budget Ch0 Dimm1	255
Warm Budget Ch1 Dimm0	255
Warm Budget Ch1 Dimm1	255
Hot Budget Ch1 Dimm0	255
Hot Budget Ch1 Dimm1	255

Range[0;255]=[0;31.875] In W,  
(255= 31.875W= Def)

- ←: Select Screen
- ↑↓: Select Item
- Enter: Select
- +/-: Change Opt.
- F1: General Help
- F2: Previous Values
- F9: Optimized Defaults
- F10: Save & Exit
- ESC: Exit



Memory RAPL

RAPL Power Floor Ch0	0
RAPL Power Floor Ch1	0
RAPL PL Lock	[Disabled]
RAPL PL 1 Enable	[Disabled]
RAPL PL 1 Power	0
RAPL PL 1 WindowX	0
RAPL PL 1 WindowY	0
RAPL PL 2 Enable	[Disabled]
RAPL PL 2 Power	220
RAPL PL 2 WindowX	1
RAPL PL 2 WindowY	10

Enable or disable lock Rapl Limit register.

- ←→: Select Screen
- ↑↓: Select Item
- Enter: Select
- ←/→: Change Opt.
- F1: General Help
- F2: Previous Values
- F5: Optimized Defaults
- F10: Save & Exit
- ESC: Exit

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GT - Power Management Control

GT Info

GT2: (600 MHz)

Check to enable render standby support.

RC6 (Render Standby)

[Disabled]

GT OverClocking Support

[Disabled]

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
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Chipset

Intel PCH RC Version 2.5.0.0  
Intel PCH SKU Name Premium SKU(BDW-U)  
Intel PCH Rev ID 03/62

- ▶ PCI Express Configuration
- ▶ USB Configuration
- ▶ PCH Azalia Configuration
- ▶ BIOS Security Configuration
- ▶ LPSS Configuration
- ▶ Audio DSP Configuration
- ▶ SB Porting Configuration

Debug Port Selection [Disabled]  
Toggle EC [Disabled]  
PCH LAN Controller [Enabled]  
LAN PHY Drives GPIO27 [Disabled]  
Wake on LAN [Disabled]  
SLP\_LAN# Low on DC Power [Disabled]  
Wake on WLAN Enable [Disabled]  
Wake on WLAN Enable From DeepSx [Disabled]  
GP27 Wake From DeepSx [Disabled]  
PCIe Wake From DeepSx [Disabled]  
EC Turbo Control Mode [Enabled]  
CLRDRUN# Logic [Continuous]  
Serial IRQ Mode

PCI Express Configuration settings

- ←→: Select Screen
- ↑↓: Select Item
- Enter: Select
- +/-: Change Opt.
- F1: General Help
- F2: Previous Values
- F9: Optimized Defaults
- F10: Save & Exit
- ESC: Exit

## Chipset

- ▶ PCI Express Configuration
- ▶ USB Configuration
- ▶ PCH Azalia Configuration
- ▶ BIOS Security Configuration
- ▶ LPSS Configuration
- ▶ Audio DSP Configuration
- ▶ SB Porting Configuration

Debug Port Selection	[Disabled]
Toggle EC	[Disabled]
PCH LAN Controller	[Enabled]
LAN PHY Drives GPIO27	[Disabled]
Wake on LAN	[Disabled]
SLP_LAN# Low on DC Power	[Disabled]
Wake on WLAN Enable	[Disabled]
Wake on WLAN Enable from DeepSx	[Disabled]
GP27 Wake From DeepSx	[Disabled]
PCIe Wake From DeepSx	[Disabled]
EC Turbo Control Mode	[Disabled]
CLKRUN# Logic	[Enabled]
Serial IRQ Mode	[Continuous]
SB CR10	[Disabled]
PCH Cross Throttling	[Disabled]
SLP_S4 Assertion Width	[4-5 seconds]
Restore AC Power Loss	[Power Off]

▲ Select AC power state when power is re-applied after a power failure.

++: Select Screen  
F4: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Chipset

PCI Express Configuration

PCI Express Clock Gating	[Enabled]
DMI Link ASPM Control	[Enabled]
DMI Link Extended Synch Control	[Disabled]
PCI Express Root Port Function Swapping	[Disabled]
Subtractive Decode	[Disabled]

- ▶ PCI Express Root Port 1
- ▶ PCI Express Root Port 2
- ▶ PCI Express Root Port 3
- ▶ PCI Express Root Port 4
- ▶ PCI Express Root Port 5 is assigned to LAN
- ▶ PCI Express Root Port 6

Enable or disable PCI Express Clock Gating for each root port.

↑↓: Select Screen  
←→: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Chioaset

PCI Express Root Port 4	[Enabled]	Control the PCI Express Root Port.
#SPM	[Disabled]	
└─ Substates	[Disabled]	
URR	[Disabled]	
FER	[Disabled]	
NFER	[Disabled]	
CER	[Disabled]	
CTO	[Disabled]	
SEFE	[Disabled]	
SENFE	[Disabled]	
SECE	[Disabled]	
PME SCI	[Enabled]	
Hot Plug	[Disabled]	
PCIe Speed	[Auto]	
Detect Non-Compliance Device	[Disabled]	
Extra Bus Reserved	0	
Reserved Memory	10	
Prefetchable Memory	10	
Reserved I/O	4	
PCIe LTR	[Enabled]	
PCIe LTR Lock	[Disabled]	
Snoop Latency Override	[Auto]	
Non Snoop Latency Override	[Auto]	

++: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
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USB Configuration

USB Precondition	[Enabled]
XHCI Mode	[Smart Auto]
BTCG	[Enabled]
USB Ports Per-Port Disable Control	[Disabled]

Precondition work on USB host controller and root ports for faster enumeration.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
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ESC: Exit

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PCH Azalia Configuration

Azalia	[Enabled]
Azalia Docking Support	[Enabled]
Azalia PME	[Disabled]

Control Detection of the Azalia device.

Disabled = Azalia will be unconditionally disabled

Enabled = Azalia will be unconditionally Enabled

Auto = Azalia will be enabled if present, disabled otherwise.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit



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BIOS Security Configuration

SMI Lock	[Enabled]
BIOS Lock	[Disabled]
GPIO Lock	[Disabled]
BIOS Interface Lock	[Enabled]
RTC Lock	[Enabled]

Enable or disable SMI lockdown.

++: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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LPSS Configuration		Enables/Disables the LPSS Controller
DMA Controller	[Auto (depends on OS)]	
I2C0 Controller	[Disabled]	
I2C1 Controller	[Disabled]	
SPI0 Controller	[Disabled]	
SPI1 Controller	[Disabled]	
UART0 Controller	[Disabled]	
UART1 Controller	[Disabled]	
SVID Controller	[Disabled]	
LPSS Mode	[ACPI Mode]	

++: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F9: Optimized Defaults  
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Audio DSP Configuration

Audio DSP

[Disabled]

Control of the Audio DSP device.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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Audio DSP Configuration

Audio DSP	[Enabled]
Audio DSP Non-CS Mode	[Disabled]
Audio DSP D3 Power Gating	[Disabled]
Audio DSP Codec Select	[Realtek ALC5640]
Bluetooth Support	[Disabled]
Audio DSP Mode	[ACPI Mode]
Audio DSP OEM Feature Mask	
LPAL [BIT0]	[Disabled]
Waves [BIT1]	[Disabled]
DTS [BIT2]	[Disabled]

Control of the Audio DSP device.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

Password Description

If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup.

If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights.

The password length must be

In the following range:

Minimum length	3
Maximum length	20

Administrator Password

User Password

HDD Security Configuration:

P0: Samsung SSD

P1: Samsung SSD

- ▶ Secure Boot menu
- ▶ Secure Flash update

Set Administrator Password

++: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit

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Main Advanced Chipset Security **Boot** Save & Exit

Boot Configuration

Setup Prompt Timeout

3

Bootup NumLock State

[On]

Quiet Boot

[Enabled]

Fast Boot

[Disabled]

Boot Option Priorities

Boot Option #1

[P0: Samsung SSD 860  
EVO mSATA ]

Hard Drive BBS Priorities

Number of seconds to wait for  
setup activation key.  
65535(0xFFFF) means indefinite  
waiting.

←→: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F9: Optimized Defaults  
F10: Save & Exit  
ESC: Exit