

```
# ---Set GPIO pin from unused pwmgens to Enable pin---

setp hm2_7i92.0.gpio.005.is_opendrain 1
setp hm2_7i92.0.gpio.005.is_output true
setp hm2_7i92.0.gpio.005.invert_output true

net y-enable joint.1.amp-enable-out => hm2_7i92.0.pwmgen.01.enable => hm2_7i92.0.gpio.005.out
```

loadrtconfig = "num_pwmgens = 4

~\$ halcmd show all hm2

```
Parameter Aliases:
Alias                                Original Name
hm2_7i92.0.encoder.00.sel0.invert_output  hm2_7i92.0.gpio.010.invert_output
hm2_7i92.0.encoder.00.sel0.is_opendrain   hm2_7i92.0.gpio.010.is_opendrain
hm2_7i92.0.pwmgen.00.out0.invert_output    hm2_7i92.0.gpio.008.invert_output
hm2_7i92.0.pwmgen.00.out0.is_opendrain     hm2_7i92.0.gpio.008.is_opendrain
hm2_7i92.0.pwmgen.00.out1.invert_output    hm2_7i92.0.gpio.009.invert_output
hm2_7i92.0.pwmgen.00.out1.is_opendrain     hm2_7i92.0.gpio.009.is_opendrain
hm2_7i92.0.pwmgen.01.out0.invert_output    hm2_7i92.0.gpio.006.invert_output
hm2_7i92.0.pwmgen.01.out0.is_opendrain     hm2_7i92.0.gpio.006.is_opendrain
hm2_7i92.0.pwmgen.01.out1.invert_output    hm2_7i92.0.gpio.007.invert_output
hm2_7i92.0.pwmgen.01.out1.is_opendrain     hm2_7i92.0.gpio.007.is_opendrain
hm2_7i92.0.pwmgen.02.out0.invert_output    hm2_7i92.0.gpio.004.invert_output
hm2_7i92.0.pwmgen.02.out0.is_opendrain     hm2_7i92.0.gpio.004.is_opendrain
hm2_7i92.0.pwmgen.02.out1.invert_output    hm2_7i92.0.gpio.005.invert_output
hm2_7i92.0.pwmgen.02.out1.is_opendrain     hm2_7i92.0.gpio.005.is_opendrain
hm2_7i92.0.pwmgen.03.out0.invert_output    hm2_7i92.0.gpio.002.invert_output
hm2_7i92.0.pwmgen.03.out0.is_opendrain     hm2_7i92.0.gpio.002.is_opendrain
hm2_7i92.0.pwmgen.03.out1.invert_output    hm2_7i92.0.gpio.003.invert_output
hm2_7i92.0.pwmgen.03.out1.is_opendrain     hm2_7i92.0.gpio.003.is_opendrain
hm2_7i92.0.sserial.00.tx0.invert_output    hm2_7i92.0.gpio.001.invert_output
hm2_7i92.0.sserial.00.tx0.is_opendrain     hm2_7i92.0.gpio.001.is_opendrain
```

7i85sman.pdf

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CONNECTORS

OUTPUT/SERIAL CONNECTOR TB1

Connector TB1 is a 3.5MM pluggable screw terminal block

TB1 PIN	FUNCTION	DIR
1	GND	FROM 7i85S
2	GND	FROM 7i85S
3	TX4,STEP2-	FROM 7i85S
4	/TX4,STEP2+	FROM 7i85S
5	TX5,DIR2-	FROM 7i85S
6	/TX5,DIR2+	FROM 7i85S
7	+5V	FROM 7i85S
8	+5V	FROM 7i85S
9	GND	FROM 7i85S
10	GND	FROM 7i85S
11	TX6,STEP3-	FROM 7i85S
12	/TX6,STEP3+	FROM 7i85S
13	TX7,DIR3-	FROM 7i85S
14	/TX7,DIR3+	FROM 7i85S
15	+5V	FROM 7i85S
16	+5V	FROM 7i85S
17	GND	FROM 7i85S
18	GND	FROM 7i85S
19	SRX0	TO 7i85S
20	/SRX0	TO 7i85S