

Mesa 7177 6 channel servo board

TB2	1	VFIELD	<+24V
	2	VFIELD <td>+24V</td>	+24V
	3	VFIELD <td></td>	
	4	VFIELD <td></td>	
	5	VIN <td></td>	
	6	NC <td></td>	
	7	NC <td></td>	
	8	GROUND <td>COMMON</td>	COMMON

TB1	1	GND
	2	5V

TB3	ENCODER 0-2	wire tag
1	QA0	X-AXIS
2	/QA0	
3	GND	
4	QB0	
5	/QB0	
6	GND	
7	IDX0	
8	/IDX0	X-AXIS
9	QA1	Z-AXIS
10	/QA1	
11	GND	
12	QB1	
13	/QB1	
14	GND	
15	IDX1	
16	/IDX1	Z-AXIS
17	QA2	
18	/QA2	
19	GND	
20	QB2	
21	/QB2	
22	GND	
23	IDX2	
24	/IDX2	

wire tag

ENCODER 3-5

wire tag

TB5	ANALOG DRIVE INTERFACE	wire tag
1	ENAO-	<POWER GND
2	ENAO+	ENABLE X
3	GND	
4	AOUT0	VREF X
5	ENAI-	<POWER GND
6	ENAI+	ENABLE Z
7	GND	
8	AOUT1	VREF Z
9	ENAI-	
10	ENAI+	
11	GND	
12	AOUT2	
13	ENAI-	
14	ENAI+	
15	GND	
16	AOUT3	
17	ENAI-	
18	ENAI+	
19	GND	
20	AOUT4	
21	ENAI-	
22	ENAI+	
23	GND	
24	AOUT5	VREF VFD

SPINDLE 100 P
 NC
 NC
 NC
 NC
 SPINDLE IDX
 NC

TB6 RS-422 INTERFACE

TB7 INPUT/OUTPUT

TB8 INPUT/OUTPUT

3PH SSR

TB6	1	GND
	2 <th>GND</th>	GND
	3 <th>RX+</th>	RX+
	4 <th>RX-</th>	RX-
	5 <th>TX+</th>	TX+
	6 <th>TX-</th>	TX-
	7 <th>+V5</th>	+V5
	8 <th>-5V</th>	-5V

TB7	1	INPUT 16
	2 <th>INPUT 17</th>	INPUT 17
	3 <th>INPUT 18</th>	INPUT 18
	4 <th>INPUT 19</th>	INPUT 19
	5 <th>INPUT 20</th>	INPUT 20
	6 <th>INPUT 21</th>	INPUT 21
	7 <th>INPUT 22</th>	INPUT 22
	8 <th>INPUT 23</th>	INPUT 23
	9 <th>INPUT 24</th>	INPUT 24
	10 <th>INPUT 25</th>	INPUT 25
	11 <th>INPUT 26</th>	INPUT 26
	12 <th>INPUT 27</th>	INPUT 27
	13 <th>INPUT 28</th>	INPUT 28
	14 <th>INPUT 29</th>	INPUT 29
	15 <th>INPUT 30</th>	INPUT 30
	16 <th>INPUT 31</th>	INPUT 31
	17 <th>OUTPUT 8</th>	OUTPUT 8
	18 <th>OUTPUT 9</th>	OUTPUT 9
	19 <th>OUTPUT 10</th>	OUTPUT 10
	20 <th>OUTPUT 11</th>	OUTPUT 11
	21 <th>OUTPUT 12</th>	OUTPUT 12
	22 <th>OUTPUT 13</th>	OUTPUT 13
	23 <th>OUTPUT 14</th>	OUTPUT 14
	24 <th>OUTPUT 15</th>	OUTPUT 15

TB8	1	INPUT 0
	2 <th>INPUT 1</th>	INPUT 1
	3 <th>INPUT 2</th>	INPUT 2
	4 <th>INPUT 3</th>	INPUT 3
	5 <th>INPUT 4</th>	INPUT 4
	6 <th>INPUT 5</th>	INPUT 5
	7 <th>INPUT 6</th>	INPUT 6
	8 <th>INPUT 7</th>	INPUT 7
	9 <th>INPUT 8</th>	INPUT 8
	10 <th>INPUT 9</th>	INPUT 9
	11 <th>INPUT 10</th>	INPUT 10
	12 <th>INPUT 11</th>	INPUT 11
	13 <th>INPUT 12</th>	INPUT 12
	14 <th>INPUT 13</th>	INPUT 13
	15 <th>INPUT 14</th>	INPUT 14
	16 <th>INPUT 15</th>	INPUT 15
	17 <th>OUTPUT 0</th>	OUTPUT 0
	18 <th>OUTPUT 1</th>	OUTPUT 1
	19 <th>OUTPUT 2</th>	OUTPUT 2
	20 <th>OUTPUT 3</th>	OUTPUT 3
	21 <th>OUTPUT 4</th>	OUTPUT 4
	22 <th>OUTPUT 5</th>	OUTPUT 5
	23 <th>OUTPUT 6</th>	OUTPUT 6
	24 <th>OUTPUT 7</th>	OUTPUT 7

5SR 10A

VFD FORWARD
 VFD REVERSE

EXT ESTOP

LIMIT/HOME X
 LIMIT/HOME Z

ESTOP OUT