Pin	CPU Pin	Pin Name	Pinmux register offset	mode 6	mode 7		GPIO Bank	GPIO Pin	Probotix PBX-BB	Probotix PBX-BB 5.1	Kernel GPIO Number	hal_pru_generic GPIO pin number	hal_bb_gpio pin number	Proposed GPIO pin number
1	GND													
2	GND													
3	3.3V													
4	3.3V													
5	VDD_5V													
6	VDD_5V													
7	SYS_5V													
8	SYS_5V													
9	PWR_BUT													
10	A10	WARMRSTn	0x9B8											
11	T17	GPMC_WAIT0	0x870	uart4_rxd	*gpio0_30		0	30		Tool_Sensor	30	62	211	911
12	U18	GPMC_BEn1	0x878	mcasp0_aclkr	*gpio1_28		1	28		LCD_LED	60	92	212	912
13	U17	GPMC_WPn	0x874	uart4_txd	*gpio0_31		0	31		Z_Probe	31	63	213	913
14	U14	GPMC_A2	0x848	ehrpwm1A	*gpio1_18		1	18	PWM3	LCD_PWM	50	82	214	914
15	R13	GPMC_A0	0x840	ehrpwm1_tripzone_input	*gpio1_16		1	16		LCD_Left	48	80	215	915
16	T14	GPMC_A3	0x84C	ehrpwm1B	*gpio1_19		1	19		LCD_Up	51	83	216	916
17	A16	SPI0_CS0	0x95C	pr1_edio_data_out1	*gpio0_5		0	5		Y2_Step	5	37	217	917
18	B16	SPI0_D1	0x958	pr1_edio_data_out0	*gpio0_4		0	4		Y2_Dir	4	36	218	918
19	D17	UART1_RTSn	0x97C	pr1_edc_latch1_in	*gpio0_13		0	13			13	45	219	919
20	D18	UART1_CTSn	0x978	pr1_edc_latch0_in	*gpio0_12		0	12			12	44	220	920
21	B17	SPI0_D0	0x954	EMU3	*gpio0_3		0	3		Spindle_On	3	35	221	921
22	A17	SPI0_SCLK	0x950	EMU2	*gpio0_2		0	2		A_Step	2	34	222	922
23	V14	GPMC_A1	0x844	ehrpwm0_synco	*gpio1_17		1	17		LCD_Right	49	81	223	923
24	D15	UART1_TXD	0x984	pr1_pru0_pru_r31_16	*gpio0_15		0	15		LCD_Enter	15	47	224	924
25	A14	MCASP0_AHCLKX	0x9AC	pr1_pru0_pru_r31_7	*gpio3_21		3	21	ZDIR	In_0	117	149	225	925
26	D16	UART1_RXD	0x980	pr1_pru1_pru_r31_16	*gpio0_14		0	14		A_Dir	14	46	226	926
27	C13	MCASP0_FSR	0x9A4	pr1_pru0_pru_r31_5	*gpio3_19		3	19	ADIR / Y2DIR	LCD_Enable	115	147	227	927
28	C12	MCASP0_AHCLKR	0x99C	pr1_pru0_pru_r31_3	*gpio3_17		3	17	YDIR / Y1DIR	In_2	113	145	228	928
29	B13	MCASP0_FSX	0x994	pr1_pru0_pru_r31_1	*gpio3_15		3	15	XDIR	Out_0	111	143	229	929
30	D12	MCASP0_AXR0	0x998	pr1_pru0_pru_r31_2	*gpio3_16		3	16	YSTP / Y1STP	LCD_Down	112	144	230	930
31	A13	MCASP0_ACLKX	0x990	pr1_pru0_pru_r31_0	*gpio3_14		3	14	XSTP	Out_1	110	142	231	931
32	VADC													
33	C8	AIN4												
34	AGND													
35	A8	AIN6												
36	B8	AIN5												
37	B7	AIN2								LCD_Touch				
38	A7	AIN3								LCD_Touch				
39	B6	AIN0								LCD_Touch				
40	C7	AIN1								LCD_Touch				
41	D14	XDMA_EVENT_INTR1	0x9B4	EMU3	*gpio0_20		0	20	ASTP / Y2STP	In_1	20	52	241	941
41.1	D13	MCASP0_AXR1	0x9A8	pr1_pru0_pru_r31_6	*gpio3_20		3	20			116	148		991
42	C18	ECAP0_IN_PWM0_OUT	0x964	xdma_event_intr2	*gpio0_7		0	7	ZSTP	Out_2	7	39	242	942
42.1	B12	MCASP0_ACLKR	0x9A0	pr1_pru0_pru_r31_4	*gpio3_18		3	18			114	146		992
43	GND													
44	GND													
45	GND													
46	GND													